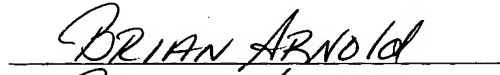



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Raimund Kram, et al.
Serial No. : To Be Assigned
Filed : June 25, 2004
For : ERROR TOLERANT AUTOMATION SYSTEM AND
METHOD FOR ERROR HANDLING IN A REAL-TIME
AUTOMATION SYSTEM
Examiner : To Be Assigned
Group Art Unit : To Be Assigned

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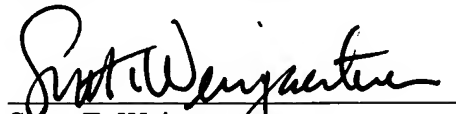
Applicants submit this communication and the attached Form PTO/SB/08B in compliance with 37 C.F.R. §§1.56, 1.97 and 1.98. A copy of each of the documents listed on Form PTO/SB/08B is enclosed with this communication.

The referenced application is the U.S. national stage application of international application no. PCT/DE02/04711, filed December 23, 2002, a copy of which is attached. The documents identified on the Form PTO/SB/08B (attached herewith) include documents cited in the International Search Report (ISR), a copy of which is also attached. Also attached is a copy of the International Preliminary Examination Report (IPER).

The Commissioner is hereby authorized to charge any fee which may be due in connection with this communication to Deposit Account No. 23-1703.

Dated: 6/25/04

Respectfully submitted,



Scott T. Weingaertner
Reg. No. 37,756
Attorney for Applicants

Customer No. 007470
White & Case LLP
Direct Dial: (212) 819-8404

PTO/SB/08B (08-03)

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Application Number	To Be Assigned 10/500232
Filing Date	June 25, 2004
First Named Inventor	Raimund Kram, et al.
Art Unit	To Be Assigned
Examiner Name	To Be Assigned
Attorney Docket Number	1140668-0063

Sheet

1

of

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		DAVID B. STEWART ET AL.; The Chimera II Real-Time Operating System for Advanced Sensor-Based Control Applications; IEEE Transactions on Systems, Man, and Cybernetics; Nov. /Dec., 1992; USA; Vol. 22, No. 6.	
		DAVID B. STEWART ET AL.; Dynamically Reconfigurable Embedded Software - Does it Make Sense?; Engineering of complex Computer Systems; Proceedings; Second IEEE International Conference in Montreal, Quebec-Canada; 21-25 October, 1996; p. 217-220.	
		A. TERRASA ET AL.; Including User-Defined Timing Exception Support in FRTL; Proceedings; Seventh International Conference on Real-Time Computing Systems and Applications; 12-14 December 2000; p.255-262.	

Examiner
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